

Electronic Acknowledgement Receipt

EFS ID:	5402586
Application Number:	10597919
International Application Number:	
Confirmation Number:	7894
Title of Invention:	Multilayer substrate with built-in-chip-type electronic component and method for manufacturing the same
First Named Inventor/Applicant Name:	Osamu Chikagawa
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Filer Authorized By:	Joseph Richard Keating
Attorney Docket Number:	36856.1458
Receipt Date:	27-MAY-2009
Filing Date:	11-AUG-2006
Time Stamp:	17:10:53
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	36856-1458-PTO-20090526- US_IDS_Form__SB_08a-.pdf	763337 <small>/b17C20ed71801d479e6d0272d2ce8b11d 1616ae7</small>	no	4

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2	NPL Documents	36856-1458-PTO-20090527-references.pdf	428500	no	10
			5464707-c128995a23b47bc38e10417a2e70364b9		

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National Stage of an International Application under 35 U.S.C. 371

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